

Search Notes

Application/Control No.

09/560,364

Examiner

Jason M. Perilla

Applicant(s)/Patent under
Reexamination

LO ET AL.

Art Unit

2634

SEARCHED

| Class | Subclass | Date | Examiner |
|-------|----------|-----------|----------|
| 375 | 295 | 5/25/2005 | JP |
| | 316 | 5/25/2005 | JP |
| | 354 | 5/25/2005 | JP |
| | 257 | 5/25/2005 | JP |
| 365 | 233 | 5/25/2005 | JP |
| 326 | 93 | 5/25/2005 | JP |
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INTERFERENCE SEARCHED

| Class | Subclass | Date | Examiner |
|-------|----------|-----------|----------|
| 375 | 295 | 5/25/2005 | JP |
| 365 | 233 | 5/25/2005 | JP |
| 326 | 93 | 5/25/2005 | JP |
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**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

| | DATE | EXMR |
|---|-----------|------|
| East EPO/JPO USPAT/USPGPUB | 5/25/2005 | JP |
| Inventor Name Search EDAN/EAST | 5/25/2005 | JP |
| Buffer Logic Datasheets Fairchild; Motorola; Texas Instruments | 5/25/2005 | JP |
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